

## CLAIMS

What is claimed is:

1. An apparatus comprising:  
  
at least two current sources, wherein the amount of current provided by each of the at least two current sources is based on at least one multi-bit control signal and wherein each of the at least two current sources selectively provides current in response to at least one coefficient on-state command; and  
  
a summer to sum currents of each of the at least two current sources.
2. The apparatus of Claim 1, further comprising a shift register to provide at least one coefficient on-state command in response to an input signal.
3. The apparatus of Claim 1, further comprising a bias current source to provide bias current to each of the at least two current sources.
4. The apparatus of Claim 1, further comprising a current-to-voltage converter to convert current from the summer into a voltage.
5. The apparatus of Claim 1, wherein each current represents a coefficient in a finite impulse response input-output relationship.

6. The apparatus of Claim 1, wherein a sum of currents represents an output in a finite impulse response input-output relationship.
7. A method comprising:
  - selectively providing at least two currents in response to at least one coefficient on-state command, wherein the amount of each of the two currents is based on at least one multi-bit control signal; and
  - summing each of the at least two currents.
8. The method of Claim 7, further comprising selectively providing the at least one coefficient on-state command in response to an input signal.
9. The method of Claim 7, further comprising converting the sum of currents into a voltage.
10. The method of Claim 7, wherein each current represents a coefficient in a finite impulse response input-output relationship.
11. The method of Claim 7, wherein a sum of currents represents an output in a finite impulse response input-output relationship.

12. A system comprising:

a digital signal source;

at least two current sources, wherein the amount of current provided by each of the at least two current sources is based on at least one multi-bit control signal and wherein each of the at least two current sources selectively provides current in response to at least one coefficient on-state command;

a summer to sum currents of each of the at least two current sources;

a shift register to provide at least one coefficient on-state command in response to the digital signal source; and

an analog signal receiver to receive the current sum.

13. The system of Claim 12, wherein the digital signal source comprises an audio signal source.

14. The system of Claim 12, wherein the digital signal source comprises a video signal source.

15. The system of Claim 12, wherein the digital signal source comprises a communications signal source.

16. The system of Claim 12, wherein the analog signal receiver comprises an amplifier.

17. The system of Claim 12, wherein the analog signal receiver comprises a line driver.